

Instruction Manual for the wolfSSL Example Application

Target: Renesas RX72N Envision Kit RTOS: FreeRTOS+ IoT libraries

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WHAT IS THIS DOCUMENT FOR?

This document is an instruction to add wolfSSL TLS library and to run an example program on the Renesas RX72N Envision Kit. The target MCU is expected to be used with a real-time OS when the product is installed. Therefore, this example program is provided in a configuration that uses FreeRTOS and FreeRTOS + TCP. The steps for generating and executing the program as a new project of e² studio, an IDE made by Renesas, is explained below.

EXAMPLE PROGRAM STRUCTURE

The FreeRTOS kernel and FreeRTOS + TCP protocol stack are required to execute this example program, but they are automatically prepared when creating a new e² studio project. A script that is automatically executed when the project is created, downloads the FreeRTOS-related source files from the GitHub and configures the settings necessary for operation on the evaluation board. The downloaded FreeRTOS IoT Libraries include several demo applications, and the demo application selected from them is configured to be executed.

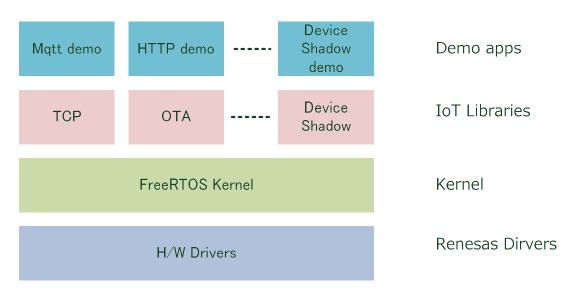


fig.1. Original Structure of the FreeRTOS+IoT libraries demos

The wolfSSL example program adds the wolfSSL library, the wolfSSL demo application, and the FIT components required as the H/W driver to this configuration, and configures it as shown in fig.2.

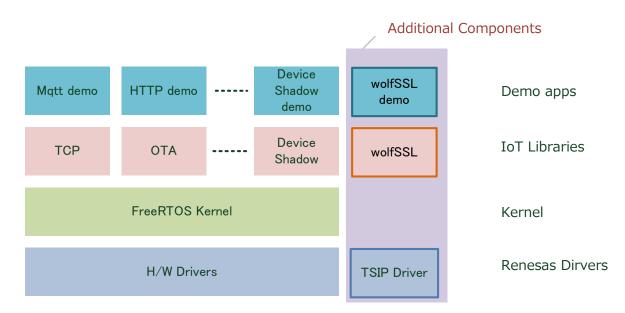


fig.2. The extended structure by adding wolfSSL demo

The added wolfSSL demo application runs as a task on the FreeRTOS kernel and utilizes the TCP protocol stack as a communication channel. In addition, the wolfSSL library supports TSIP. By replacing some of the encryption and TLS functions implemented by the wolfSSL library as software with H/W (TSIP), it is possible to significantly improve the processing speed.

REQUIREMENTS FOR BUILDING AND RUNNING THE EXAMPLE PROJECT

Tools and components required for the building and execution of this example program:

- 1. e² studio Version 2021-10 or later
- 2. CC-RX Tool Chain V3.04 or later
- 3. TSIP v1.17 or later
- 4. RTOS v202002.00-rx-1.0.1 or later
- 5. wolfSSL v5.5.4 or later

PROCEDURE FOR CREATING A WOLFSSL EXAMPLE APPLICATION PROJECT

The following steps are roughly required to execute this example program :

1. Create an executable project including FreeRTOS+IoT libraries on e²Studio

- 2. Settings for the target MCU and the evaluation board
- 3. Adding FIT components and their update
- 4. Copy wolfSSL package
- 5. Section settings
- 6. Adding wolfSSL library project and wolfSSL demo files
- 7. Execution of wolfSSL demo

From now on, the above steps will be described in that order.

1. CREATE A NEW EXECUTABLE PROJECT

Launch e² studio and specify a folder to be its workspace. The folder will be the base folder of the project. Here after, the folder is referred as **<base>** in this document.

CREATE A NEW FREERTOS PROJECT

Selecting "File" menu > "Import..." > "General" > "Renesas GitHub FreeRTOS(with IoT libraries) Project" will show you dialogs below.

| Renesas GitHub FreeRTOS (with IoT libraries) Proje O Missing RTOS Version. | ct | 11-6-0でよ。 11:0-0 11:0-0 | Ec C/C++ | | |
|---|--|--|--|---|--------------|
| Specify a folder to copy selected RTOS version in order to i Folder: F:\Work\1_Renesas\72n_demo\EN RTOS version setting | FreeRTOS (with IoT libraries) M Select RTOS modules for download a (download path should be short, for | and specify download location | | | · × |
| Version: | Title | Rev. | Issue date | ^ | Select All |
| Check for more version | FreeRTOS (with IoT libraries) FreeRTOS (with IoT libraries) FreeRTOS (with IoT libraries) | v202012.00-rza2m-1.0.0 v202012.00-re-1.0.0 v202012.00-rl78-1.0.0 | 2021-12-09 2021-10-26 2021-10-18 | | Deselect All |
| | FreeRTOS (with IoT libraries) | v202012.00-rx-1.0.0 | 2021-10-07 | | |
| | FreeRTOS (with IoT libraries) FreeRTOS (with IoT libraries) | v202002.00-rx-1.0.5 v202002.00-rx-1.0.4 v202002.00-rl78-1.0.3 v202002.00-rx-1.0.3 202002.00-rt78-1.0.2 | 2021-05-10 2021-03-16 2021-02-17 2021-01-28 2020-11-19 | | |
| | FreeRTOS (with InT libraries) Module Folder Path: | 202002.00-rx-1.0.3 | 2020-10-16 | ~ | |
| | F:\afr | | | | Browse |

fig.3. Steps to choose FreeRTOS module to download

On a FreeRTOS(with IoT libraries) Module Download dialog, select FreeRTOS with revision "v202012.00-rx-1.0.0". Next, specify the download destination folder. Note that the folder path should be short enough to avoid errors where the path length is too long.

Once the download is complete, you will be able to see the location of your project and the version of FreeRTOS, shown in fig.4.

| i folder to copy selected RTOS version in order to in F:\Work\1_Renesas\72n_demo\EN | Browse |
|--|---------|
| | biowsen |
| v202012.00-rx-1.0.0 | ~ |
| Check for more version | |
| | |
| | |
| | |
| | |
| | |
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| | |

fig.4. The project location and the version of FreeRTOS to use

The folder is identical to the workspace folder and is to be referred as <base>. For the next step, you need to choose a demo type for the target MCU and the compiler.

The dialog show in fig.5 lists up products ready for import to your <base> folder. The list contains three types(aws_demo, aws_test and boot_loader). Pick up

"aws_demos(...\projects\renesas\rx72n-envision-kit\e2studio\aws_demos)"

from the list.

| 0 | | | | | | × |
|---|---|---------------------------------|-----------------|--------|------------|----|
| Import Projects | | | | | | |
| Select a directory to sear | ch for existing Renesas projects. | | | | | |
| Select root directory: | F:\Work\1_Renesas\72n_demo\EN | | | ~ | Browse | |
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| type filter text | | | | | Select All | I. |
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fig. 5. Import Projects dialog

Script runs to extract selected source files and organize project folders in the project explore pane.

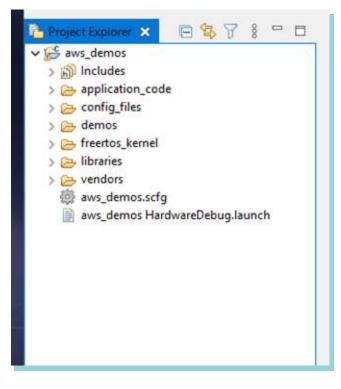


fig.6. Imported and organized aws_demos project

2.DEVICE INFO SETTINGS

Before adding FIT modules, set the board and device information. Double-click aws_demos.scfg on the Project Explorer to open the Smart Configurator Perspective and select the Board tab at the bottom to display the "Device selection" settings pane.

In the "Board" type selection list, choose "RX72NEnvisionKit(V1.10)". If no board type listed, you can get them by clicking the link named "Download more boards...".

When you choose the board, "Device" is filled with "R5F72NDHxFB" automatically.

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| > 🗁 application_code | Device selection | | |
| > 🔁 config_files | | | 1 (2003) |
| > 🗁 demos > 🕞 freertos kernel | Board: RX72NEnvisionKit (V1.10) | ~ | |
| > Carlibraries | Device: R5F572NDHxFB | | 1 |
| > Carlo output | Download more boards | | 8) |
| > Ca vendors | | | |
| aws_demos.scfg | | | |
| aws_demos HardwareDebug.launch | Feature Selection | | |
| | Feature Selection | | |
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| | The configurations for each added component can | be fu | ther configured in |
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| | Pmod Connector (INTP) | | Interrupt Controll |
| | Pmod Connector 1 | | SPI Clock Synchro |
| | Pmod Connector 2 | | SPI Clock Synchro |
| | Serial Flash | | QSPI Clock Synch |
| | Overview Board Llocks System Components Pins | Inter | TUSP Hart Com |
| | Contraction of the state of the | Antest | |

fig.7. Device selection

3. ADDING FIT MODULES

At this point, the project has the source files for FreeRTOS, the IoT library and the demo application. In addition, the source files of the necessary FIT components (drivers provided by Renesas) have already been generated. However, some FIT component libraries need to be downloaded and obtained from the Renesas site.

Double-click aws_demos.scfg on the Project Explorer to open the Smart Configurator Perspective and select the "Components" tab at the bottom to display the "Software components configuration" pane. Then push the icon to show "Software Component Selection" dialog shown in fig.8.

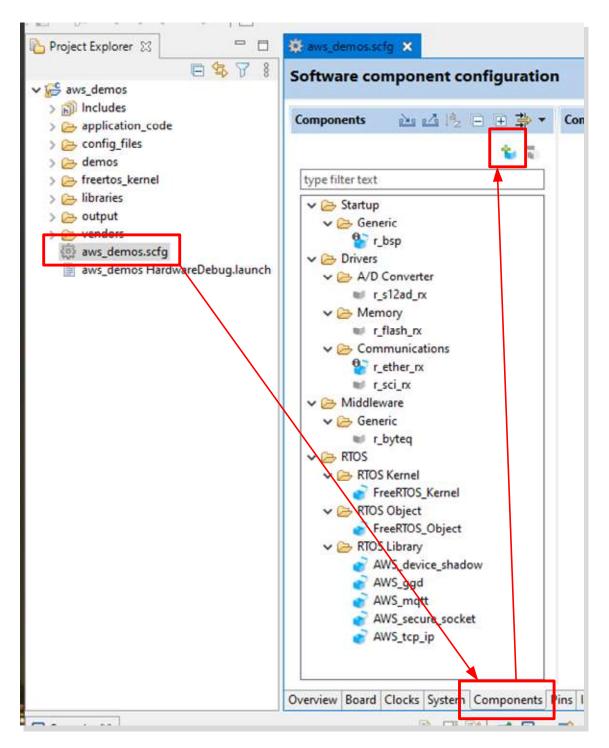


fig.8. Software components configuration

In the "Software Component Selection" dialog, find and choose one FIT component to add the project. If no components are listed in the dialog, it means that you need to download FIT components from Renesas site into your PC. Click the "Download the latest FIT drivers and middleware" link.

| Beard Support Packages. r_bsp Firmware Integr 6.20 Buses Code Generator 1.10.0 Clock Frequency Accuracy Measuremen Code Generator 1.10.0 Compare Match Timer Code Generator 2.2.0 Complementary PWM Mode Timer Code Generator 1.10.0 Convert the TCP/IP(T4) - RX Ethernet Dri r_t4_driver_rx Firmware Integr 1.08 CRC Calculator Code Generator 1.10.0 D/A Converter Code Generator 1.10.0 | Category | All | | | | |
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| Board Support Packages.r_bspFirmware Integr6.20BusesCode Generator1.10.0Clock Frequency Accuracy MeasuremenCode Generator1.10.0Compare Match TimerCode Generator2.2.0Complementary PWM Mode TimerCode Generator1.10.0Continuous Scan Mode S12ADCode Generator1.10.0Convert the TCP/IP(T4) - RX Ethernet Drir_t4_driver_rxFirmware Integr1.08CRC CalculatorCode Generator1.10.0D/A ConverterCode Generator1.10.0Data Operation CircuitCode Generator1.10.0 | Compor | nents | Short Name | Туре | Version | , |
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| | D/A (| Converter | | Code Generator | 1.10.0 | |
| A Show only latest version | 🖶 Data | Operation Circuit | | Code Generator | 1.10.0 | |
| Pescription This software component generates two units (unit 0, unit 1) of an on-chip 8-bit timer (TMR) module that comprise two 8-bit counter channels, totaling four channels. | escriptio This soft | n ware component generates two units | | on-chip 8-bit timer (TMR) | module | 1 |

fig.9. Software Component Selection Dialog

If you have downloaded the latest FIT components in your PC, you can extract the components to add in the list by specifying the function type of the component.

wolfSSL demo requires following FIT components to add the project:

1. TSIP component(r_tsip_rx)

CMT component(r_cmt_rx)

Let's take an example of how to add a TSIP component. Select "Security" function category in the following dialog lists up TSIP in the component list. Click the TSIP in the list and then push "Finish" button to add the component. Since you can add only one FIT component at a time, repeat the same steps to add other components.

| New Component | A KANDER III | | |
|---|---------------|-----------------|----------|
| oftware Component Selection | | | _ |
| Select component from those available i | n list | | ₩ |
| 82 JA | | | |
| Category All | | | × |
| Function Security | | | ~ |
| Filter | | | |
| Components | Short Name | Туре | Version |
| TSIP(Trusted Secure IP) driver. | r_tsip_rx | Firmware Integr | 1.14.1 |
| Show only latest version Description | | | |
| | | | ^ |
| Download the latest FIT drivers and mid Configure general settings | dleware | | |
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fig.10. How to add TSIP

After adding TSIP and CMT driver to the project, you can see those components are listed in the components pane.

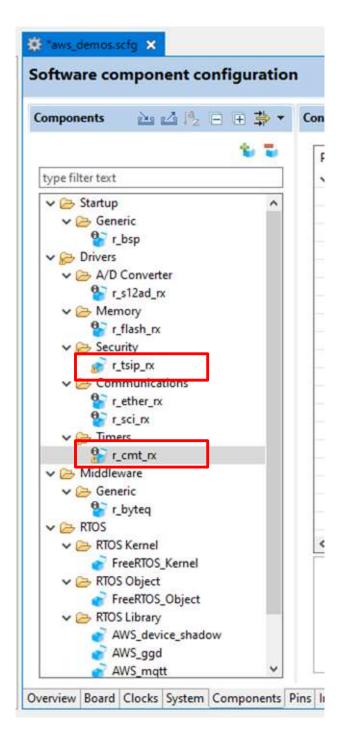


fig.11. Component list

After specifying the required FIT components, let SMC(SMart Configurator) generate source files. Push the button on the top right of the "Software component configuration" pane. Generated files are added to the aws_demos project.

4.COPY WOLFSSL PACKAGE

If you have a wolfSSL package downloaded from the GitHub or wolfSSL download page, it has version string in its top folder name(such as wolfssl-5.1.1-stable) like as the right box of fig.12. Copy the entire package under the
base> folder with the name "wolfssl".

This is important because both wolfSSL demo and aws_demos refer each other by traversing their path names. Therefore, name of wolfssl top folder and the location should be exact the same as fig.12.

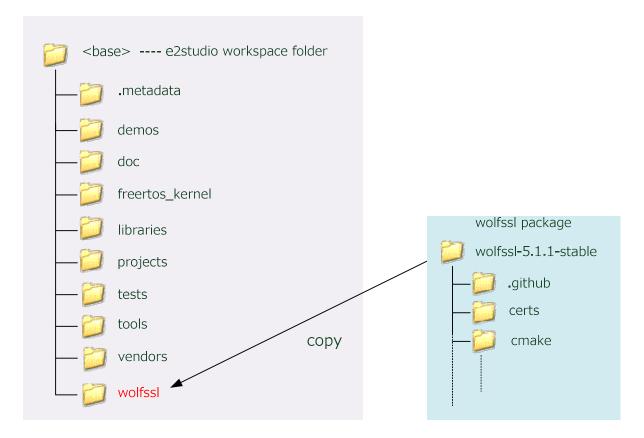


fig.12. folder structure after wolfssl package added

5.SECTION SETTING

Section setting in the memory map is necessary. Open the property page of the aws_demos project, then choose "C/C++ Build" > "Settings" > "Linker" > "Section" to show section setting pane. Push the button located in the right most of the pane to show the "Section Viewer" dialog.

Push the "Import..." button to show the dialog for specifying the section setting file to import.

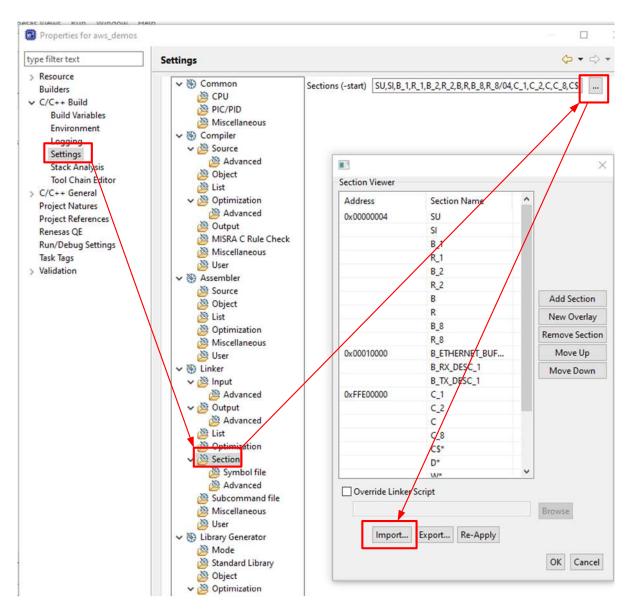


fig.13. How to show the "Section Viewer" dialog

In the dialog for a section file, specify the following file:

<base>\wolfSSL\IDE\Renesas\e2studio\RX72N\EnvisionKit\resource\section.esi

6.ADDING WOLFSSL LIBRARY AND WOLFSSL DEMO FILES

The next thing you need to do is add the wolfSSL library project and the code for the wolfSSL demo application to the aws_demos project.

IMPORTING WOLFSSL LIBRARY PROJECT

Then import the project that builds the wolfSSL library into the project explorer on e² studio. The wolfSSL library project is already available as an e² studio project in the wolfSSL package.

On e² studio, selecting "File" menu > "Open a project from the file system" > "Directory" pops up the dialog for specifying a folder including a project file. Specify the following folder:

<base>\wolfssl\IDE\Renesas\e2studio\RX72N\EnvisionKit\wolfssl

The dialog finds out a wolfssl project to import.

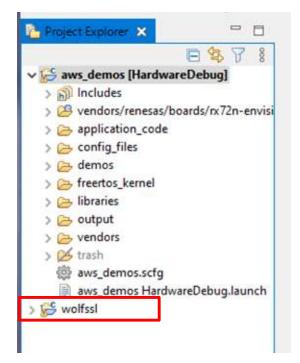


fig.14. Project structure after wolfssl project imported

You can see that the wolfssl project has been added in the project explorer. In the added wolfssl library project, there is nothing to set because the path to the include files generated by the aws_demos project is already set.

ADDING WOLFSSL DEMO APPLICATION FILES

Add wolfSSL demo application files which work as a kind of FreeRTOS demo task. Point to the aws_demos folder on the project explorer pane, then open the floating dialog by right button click to create a new source file folder named "wolfSSL demo".

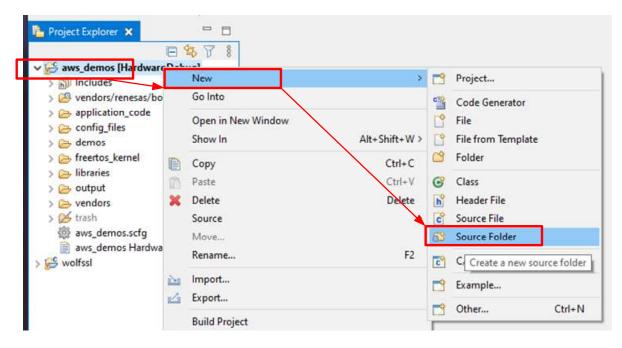


fig.15. Creating a source folder for wolfssl_demo

Open the following folder with explorer and grab all the files(*.c, *.h) in there and drop them on the created "wolfssl_demo" folder in the project explorer pane of the e^2 studio.

<base>\wolfssl\IDE\Renesas\e2studio\RX72N\EnvisionKit\wolfssl_demo

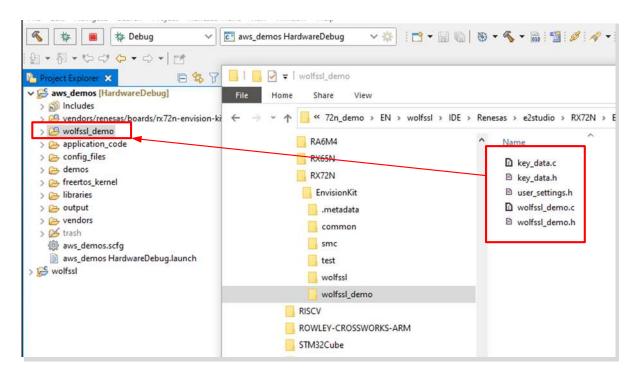


fig.16. Adding wolfssl_demo source files

You will be asked whether you want to process these files by copying or linking. Choose linking.

| Project Explorer X E % For aws_demos [HardwareDebug] Includes Vendors/renesas/boards/rx72n-envisio Wolfssl_demo Project Explorer | |
|--|--|
| > > /> freertos_kernel > >> libraries > >> output > >> /> vendors > //> trash > aws_demos.scfg | Select how files should be imported into the project: Copy files Link to files |
| iaws_demos HardwareDebug.launch > | Create link locations relative to: PROJECT_LOC Configure Drag and Drop Settings OK Cancel |

fig.17. Copying files by linking

Add more files below to the wolfssl_demo folder by linking:

- 1. <base>\wolfssl\wolfcrypt\benchmark\benchmark.c
- 2. <base>\wolfssl\wolfcrypt\benchmark\benchmark.h
- 3. <base>\wolfssl\wolfcrypt\test\test.c
- 4. <base>\wolfssl\wolfcrypt\test\test.h

Finally, you should see the wolfssl_demo folder in the project explorer, as shown in fig.18.

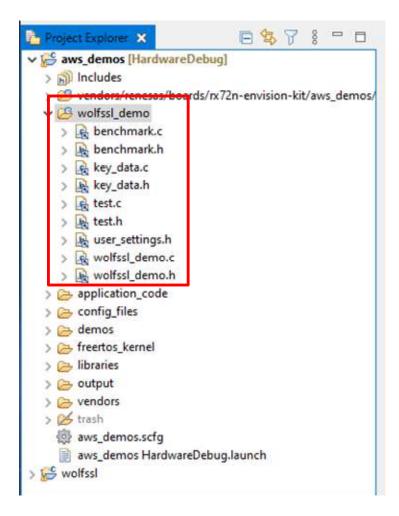


fig.18. files in the wolfssl_demo folder

ADDING INCLUDE FILE PATHS TO THE PROJECT

Open aws_demos project property setting dialog, then select "C/C++ build" > "Settings" > "Compiler" > "Source" to show "include file directories" pane. Add following include file paths:

- \${ProjDirPath}/../../../../wolfssl
- \${ProjDirPath}....../.../.../wolfssl/IDE/Renesas/e2studio/RX72N/EnvisionKit/wolfssl_demo

ADDING PREPROCESSOR MACRO

Open aws_demos project property setting dialog, then select "C/C++ build" > "Settings" > "Compiler" > "Source" to show "Macro definition" pane. Add following macro definition:

WOLFSSL_USER_SETTINGS

This macro definition lets wolfSSL demo application refer the user_settings.h file.

ADDING LINK LIBRARY

Open aws_demos project property setting dialog, then select "C/C++ build" > "Settings" > "Linker" > "Input" to show "Relocateable files, objects files and library files" pane. Add following library file:

\${ProjDirPath}/../../../wolfssl/IDE/Renesas/e2studio/RX72N/EnvisionKit/wolfssl/Debug/w olfssl.lib

7. ADDING WOLFSSL DEMO AS A TASK

wolfSSL_demo has been added as one of the demo applications to the project but not enabled yet. To do this, enable the demo and register its entry function to the demo runner environment. Open the following configuration file with editor.

<base>\venders\renesas\boards\rx72n-envision-kit\aws_demos\config_files\aws_demo_config.h

In the file, find "CONFIG_CORE_MQTT_DEMO_ENABLED" macro definition and make it commented out. Instead add definition of "CONFIG_WOLFSSL_DEMO_ENABLED" macro to set wolfssl demo enable.

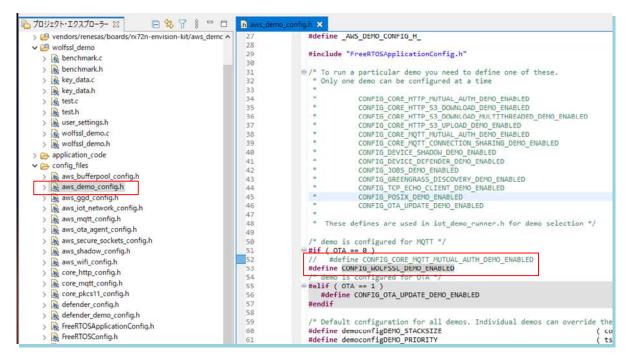


fig.19. Add macro in the configuration file

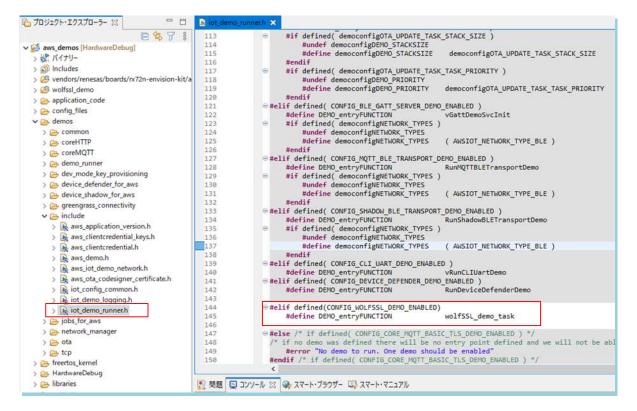
Moreover, open the following file.

<base>\demos\include\iot_demo_runner.h

Then add the two lines of code below at just before the last #else statement of the file.

#elif defined(CONFIG_WOLFSSL_DEMO_ENABLED) #define DEMO_entryFUNCTION

wolfSSL_demo_task





Those addition registers the wolfSSL_demo to run.

EXECUTION OF THE WOLFSSL DEMO APPLICATION

Internally, wolfssl_demo has three different types of demos which is selectable by the following definitions in the wolfssl_demo.h.

- #define CRYPT_TST
- #define BENCHMARK
- #define TLS_CLIENT

By enabling one of the three definition and rebuild of aws_demo project changes the demo.

Whenever you make any change in user_settings.h, rebuild wolfssl project followed by aws_demos project. The execution of the demo can use debugger in the board and monitor the output from demo through "Renesas debug virtual console".

CRYPTO-TEST DEMO

You will see the following output in the Renesas Debug Virtual Console when you choose crypto test demo.

| 🛄 Renesa | s Debug Virtual Console 🗙 🙁 変数 🎭 ブレークポイント 🛋 モジュール 🍋 フ |
|-----------|---|
| | |
| wolfSSL | version 5.1.1 |
| | |
| error | test passed! |
| MEMORY | test passed! |
| base64 | test passed! |
| | test passed! |
| RANDOM | test passed! |
| MD5 | test passed! |
| MD4 | test passed! |
| SHA | test passed! |
| SHA-256 | test passed! |
| SHA-512 | test passed! |
| Hash | test passed! |
| HMAC-MD5 | test passed! |
| HMAC-SHA | test passed! |
| HMAC-SHAD | 256 test passed! |
| HMAC-SHAS | 12 test passed! |
| | test passed! |
| Rabbit | test passed! test passed! |
| | |
| | test passed! |
| | test passed! |
| AES192 | test passed! |
| AES256 | test passed! |
| | er test passed! |
| | 9 test passed! |
| | test passed! |
| | test passed! |
| | llback test passed! |
| Test comp | |
| End wolf(| rypt Test |
| | |

fig.21. Output from Crypt-test demo

CRYPTO-BENCHMARK DEMO

You will see the following output in the Renesas Debug Virtual Console when you choose cryptobenchmark demo.

| | | | | | | | r* 🕞 🖼 🖉 | LITH | |
|--|-------|--------------------------|-----------|------------|---------|----------|----------------------------|---------|---------|
| tart wolfCrypt Benc | hmark | | | | | | | | |
| | | | | | | | | | |
| wolfSSL version 5.1 | .1 | | | | | | | | |
| olfCrypt Benchmark | (hlad | k huter 102 | 4 min 1 (| 2 coc osci | | | | | |
| | | took 1.005 | | | | | | | |
| ES-128-CBC-enc | 1 MB | took 1.002 | seconds, | 1 438 | | | | | |
| ES-128-CBC-dec | 1 MB | took 1.016 | seconds, | 1,393 | MB/s | | | | |
| | | took 1.004 | | | | | | | |
| ES-192-CBC-dec | | | | | | | | | |
| | | | | | | | | | |
| ES-256-CBC-dec | 1 MB | took 1.008 took 1.008 | seconds, | 1 187 | MB/c | | | | |
| ES-128-GCM-enc 6 | | | | | | | | | |
| ES-128-GCM-dec 6 | | | | | | | | | |
| ES-192-GCM-enc 6 | | | | | | | | | |
| | | took 1.009 | | | | | | | |
| ES-256-GCM-enc 6 | | | | | | | | | |
| ES-256-GCM-dec 6 | | | | | | | | | |
| | | took 1.000 | | | | | | | |
| | | took 1.003 | | | | | | | |
| | | took 1.040 | | | | | | | |
| | | took 1.001 | | | | | | | |
| | | took 1.000 | | | | | | | |
| | | took 1.002 | | | | | | | |
| | | took 1.002 | | | | | | | |
| | | took 1.001 | | | | | | | |
| | | took 1.000 | | | | | | | |
| | | took 1.000 | | | | | | | |
| | | took 1.002 | | | | | | | |
| | | tes took 1.020 | | | | -1- | | | |
| SA 2048 public | 12 Dy | 94 ops to | | | | | ane/see | | |
| SA 2048 private | | 2 ops to | | | | | | | |
| CC [SECP256 | | | | | | | 172.467 ms, | 5 709 0 | ne lene |
| | | 256 agree | | | | | 172.300 ms, | | |
| CDHE [SECP256 CDSA [SECP256 | | 256 agree 256 sign | | | | | 172.300 ms, 174.000 ms, | | |
| CDSA [SECP256 CDSA [SECP256 | 011 | 256 Venify | | 4 ops to | 1 1 220 | sec, avg | 332.500 ms, | 3 008 0 | ns/sec |
| URVE 25519 key gen | | | | | | | | 3.000 0 | ps/sec |
| URVE 25519 key gen URVE 25519 agree | | 4 ops to 4 ops to | ook 1.01/ | sec, avg | 254.525 | ms, 5.95 | 2 ops/sec | | |
| enchmark complete | | 4 ops to | OUK 1.015 | sec, avg | 233.750 | m5, 5.94 | ops/sec | | |
| nd wolfCrypt Benchm | ack | | | | | | | | |

fig.22. output from Crypt-benchmark demo

TLS-CLIENT DEMO

When you attempt to run TLS_Client demo, prepare the communication opponent program(TLS server program). wolfSSL package provides TLS server example application for this purpose. The application is generated by building wolfSSL package. You can build wolfSSL on Linux(including MacOS and WSL) with gcc installed or build using Visual Studio. The following introduces the build on WSL.

(1) When using ECDSA certificates

Since USE_ECC_CERT is defined in user_settings.h on the TLS_CLIENT side, it is set to use the Build the server-side sample program accordingly with the following configuration options. Don't forget to give "-DNO_RSA".

\$ cd <base>/wolfssl

\$./autogen.sh

\$./configure --enable-ecc --enable-dsa CFLAGS="-DWOLFSSL_STATIC_RSA -DHAVE_DSA -DHAVE_ALLCURVES -DHAVE_ECC -DHAVE_AESCCM -DNO_RSA"

\$ make

\$ ifconfig

(2) When using RSA certificates

Also, when setting to use RSA certificates on the TLS_CLIENT side, comment out the USE_ECC_CERT definition in user_settings.h and rebuild. Correspondingly, the server-side sample builds with the following configuration options:

| \$ cd <base/> /wolfssl |
|--|
| \$./autogen.sh |
| \$./configureenable-eccenable-dsa CFLAGS="-DWOLFSSL_STATIC_RSA -DHAVE_DSA -DHAVE_ALLCURVES -DHAVE_ECC -DHAVE_AES_CBC -DHAVE_AESCCM" |
| \$ make |
| \$ ifconfig |

If "make" command reports no error, TLS server application is ready to run. Before running the server application, get IP address of the server by typing "ifconfig". You could see IP v4 address in the console. Set the address to the TLSSERVER_IP macro defined in wolfSSL_demo.c.

The IP address of the target board could be set by changing value of the following macros:

ipconfigUSE_DHCP defined in FreeRTOSIPCOnfig.h

configIP_ADDRo ~ configIP_ADDR3 defined in FreeRTOSConfig.h

For debugging purpose or when you get trouble in TCP connection, try to use static IP address for the board.

Run the TLS server application with the following command and options. The option "-v4" specifies to use the TLS1.3 protocol. If "-v3" is specified, the TLS1.2 protocol is used.

```
$ ./examples/server/server -b -v4 -i
```

The sever application waits for the client connection. Run the demo on the board to establish TLS communication with the server application. You will see the following output in the Renesas Debug Virtual Console.

TLS Client attempt to establish TLS connection six times with TLS server using six different cipher suites respectively. The cipher suites included in the output of the TLS client depends on the TLS version and the certificate type setting.

| 🖾 Renesas Debug Virtual Console 🗴 | of 🕞 🖉 🖉 🚰 🚮 🚺 |
|---|----------------|
| /**/ | |
| TLS_Client demo | |
| - TLS server address:192.168.1.14 port: 11111 | |
| - with TSIP | |
| /**/ | |
| cipher : ECDHE-RSA-AES128-GCM-SHA256 | |
| Received: I hear you fa shizzle! | |
| cipher : ECDHE-RSA-AES128-SHA256 | |
| Received: I hear you fa shizzle! | |
| cipher : AES128-SHA | |
| Received: I hear you fa shizzle! | |
| cipher : AES128-SHA256 | |
| Received: I hear you fa shizzle! | |
| cipher : AES256-SHA | |
| Received: I hear you fa shizzle! | |
| cipher : AES256-SHA256 | |
| Received: I hear you fa shizzle! | |
| End of TLS_Client demo. | |
| | |
| | |

fig.23. output from TLS-Client demo

THINGS TODO WHEN USING USER'S ROOT CA CERT

The Root CA certificate, the server certificate and the client certificate used in this example application, can be used only for evaluation. If you want to use your own certificate, prepare following items:

- 1. Provisioning Key
- 2. RSA key pair for validating RootCA certificate
- 3. The signature generated by the RootCA certificate with the private ky in 2 above.
- 4. Private key corresponding to the public key included in the client certificate.

Refer to the manual provided by Renesas for how to generate them.

REQUIREMENTS FOR CLIENT AUTHENTICATION

wolfSSL supports client authentication as follows:

- Both ECDSA certificates and RSA certificates are handled by TSIP.
- (1) Loading client certificate

Use wolfSSL_use_certificate_buffer or wolfSSL_CTX_use_certificate_buffer to load client certificate.

(2) Loading client private key/public key

Type of the client certificate decides the keys to be loaded.

a) ECDSA certificate:

Load private key using tsip_use_PrivateKey_buffer.

b) RSA certificate:

Load private key using tsip_use_PrivateKey_buffer.

Load public key using tsip_use_PublicKey_buffer.

Note. In case of RSA certificate, the public key will be used for internal verification of signature process.

(3) How to generate encrypted keys

The keys (private and public keys) to be loaded should be encrypted-key format. Those keys could be generated with Renesas Secure Flash Programmer or

SecurityKeyManagementTool. Refer the section 7.5 and 7.6 of the application note named "RX Family TSIP Module Firmware Integration technology" how to operate above key wrapping tool.

(4) Macro to be defined

Define "WOLF_PRIVATE_KEY_ID" in your user_settings.h.

LIMITATIONS

WolfSSL, which supports TSIPv1.17, has the following functional restrictions.

- 1. Message packets exchanged with the server during the TLS handshake are stored in plaintext in memory. This is used to calculate the hash of handshake messages. The content will be deleted at the end of the session.
- 2. Session resumption and early data using TSIP are not supported.

The above restrictions are expected to be improved by TSIP or wolfSSL from the next version onwards.

RESOURCES

Followings are the links to the sites that contain helpful information regarding board, MCU, TSIP and wolfSSL .

RENESAS SITES

- Renesas wiki page for RX72N Envision Kit (<u>https://github.com/renesas/rx72n-envision-kit/wiki</u>)
- Renesas RX MCUs(<u>https://www.renesas.com/us/en/products/microcontrollers-microprocessors/rx-32-bit-performance-efficiency-mcus/</u>
- Renesas Trusted Secure IP Driver(TSIP) (<u>Renesas Trusted Secure IP</u> <u>Driver(TSIP)(https://www.renesas.com/us/en/software-tool/trusted-secure-ip-driver/)</u>

WOLFSSL SITES

- wolfSSL Website (<u>www.wolfssl.com</u>)
- wolfSSL Renesas page (<u>https://www.wolfssl.com/docs/renesas/</u>)
- wolfSSL TSIP support page (<u>https://www.wolfssl.com/docs/wolfssl-renesas-tsip/</u>)
- wolfSSL Renesas GitHub repo (<u>https://github.com/wolfSSL/Renesas/</u>)

SUPPORT AND CONTACT

For support inquiries and questions, please email <u>support@wolfssl.com</u>. Feel free to reach out to <u>info@wolfssl.jp</u>.